

10/100 BASE-T ETHERNET ISOLATION TRANSFORMER

P/N:16ST6149B DATA SHEET

A.GENERAL SPECIFICATIONS:

- 1.16 PIN, SMD PACKAGE WITH COMMON CHOKE TO MEET 100 MB /S SPECIFICATION.
- 2.LOW LEAKAGE INDUCTANCE AND WINDING CAPACITANCES FOR FAST RISE TIME.
- 3.MORE ECONMICAL THAN DISCRETE TRANSFORMERS.
- 4.HIGH ISOLATION VOLTAGES TO COMPLY WITH INTERNATIONAL SAFETY REQUIREMENTS FOR LAN.
- 5.OPERATING TEMP RANGE: 0°C TO+70°C
- 6.STORAGE TEMP RANGE: -25°C TO+125°C
- 7.RX (PIN6-PIN7) ALSO WITH COMMON CHOKE.

B.ELECTRICAL SPECIFICATIONS AT 25°C

- 1.TURN RATIOS ($\pm 5\%$): RX=1CT: 1CT(1-3-2 TO 7-5-6)
TX=1.4CT: 1CT(16-14-15 TO 10-12-11)
2. PRI OCL INDUCTANCE (MIN.): 350uH AT 100KHz /0.1Vrms, 8mA DC,
- 3.CW/W (pF MAX.): 25(1-2 TO 7-6) & 25 (16-15 TO 10-11)
- 4.LEAKAGE INDUCTANCE uH MAX) AT 1MHz: 0.5(1-2 SHORT 6-7)
0.5(16-15 SHORT 10-11)
- 5.DCR (OHMS MAX.): 0.7(1-2) & 0.5(16-15) & 1.7(6-7)=(10-11)
- 6.INSERTION LOSS (1MHz TO 100MHz):-1.0dB MAX.
- 7.RETURN LOSS (AT 100 \pm 15 OHMS):-16dB Min. AT 100KHz TO 30MHz.
-16dB Min. AT 30MHz TO 60MHz.
-10dB Min. AT 60MHz TO 80MHz.
- 8.CMDR: -40dBMIN. AT 100KHz TO 50MHz.
-30dBMIN. ABOVE 50MHz TO 100MHz.
- 9.CROSSTALK: -30dB MIN AT 100KHz TO 100MHz
- 10.COMMON TO COMMON MODE: -40dB MIN AT 100 KHz TO 100MHz.
- 11.HI-POT TEST (Vrms): 2000(PRI/SEC)

C.DIMENSIONS & SCHEMATIC & MARKING:

