

10 BASE T, SMD ETHERNET TRANSFORMER

P/N: TS7025 DATA SHEET

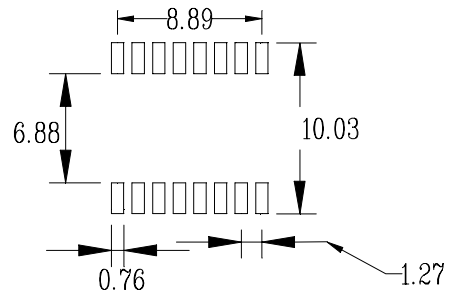
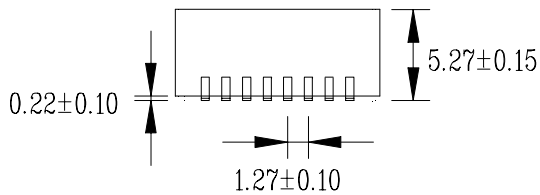
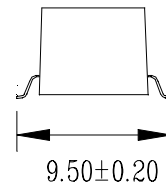
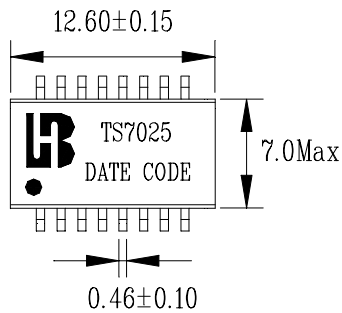
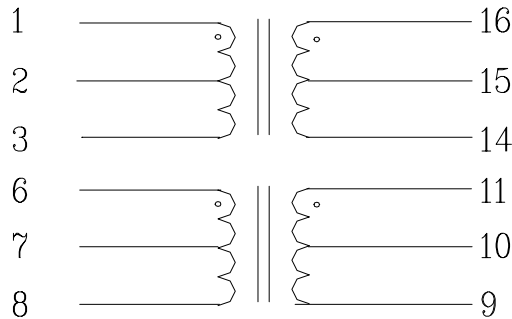
A. GENERAL SPECIFICATIONS

1. 16PIN SOIC ISOLATION MODULES.
2. MEET THE REQUIREMENTS OF IEEE802.3
3. OPERATION TEMP: 0°C TO +70°C

B. ELECTRICAL SPECIFICATIONS @ 25° C

1. TURN RATIO (1-3:16-14)=1CT: 1CT (6-8:11-9)=1CT: 2CT ±3%
2. PRIMARY OCL INDUCTANCE: 100uH ±20%
3. CW/W: 9 PF MAX
4. LL: 0.3 uH MAX
5. DCR: 0.3 OHM MAX
6. HI-POT: 1500Vrms_Min @ 0.5mA
7. INSULATION RESISTANCE: 100 MOHM MIN.

C. SCHEMATIC & DIMENSIONS & MARKING (UNIT mm):



Tolerance : ±0.25