

ISDN S-INTERFACE TRANSFORMER

P/N: S5016H-3 DATA SHEET

A. Electrical Specifications @25°C :

1. Turns Ratio($\pm 1\%$)(15KHz/0.1Vrms) : PIN9-10:2-17=2:1
 PIN8-11:4-15=2:1
 PIN1-2:18-17=5-4:14-15=1:1
2. DCR : PIN 2-17=4-15=4 Ω Max
 PIN 9-10=8-11=5.5 Ω Max
 PIN 1-2=17-18=4-5=14-15=1.3 Ω Max
3. Inductance @10KHz, 0.1Vrms : PIN 2-17=4-15=22 mH Min
 PIN1-2=17-18=4-5=14-15=5 mH +50/-30%
4. Leakage Inductance @100KHz, 0.1Vrms(PIN9-10, 8-11 Short) : PIN 2-17=4-15=4 μ H Max
5. Choke Leakage Inductance @100KHz, 0.1Vrms(PIN17-18, 14-15 Short) : PIN 1-2=4-5=0.6 μ H Max
6. Interwinding Capacitance @10KHz, 0.1Vrms PIN1-9=5-8=120pF Max
7. HI-POT : 3000Vrms/1mA 2Sec.PIN2-9, 4-8
 500Vrms/1mA 2Sec.PIN3-16(1-9, 5-8 Short)

B. Schematic & Mechanical :

